

A vertical strip of 12 small, square images showing various stages of a plant's growth, from a seedling to a mature plant.

5 individually receiving, from the exterior, a binary
logic signal corresponding to whether or not to execute
a recording operation, a recording image signal and a
clock signal, recording execution means for executing
the recording operation according to the recording
10 image signal and the clock signal entered through the
external connection terminals, in case the binary logic
signal is in a first state, data memory means for and
data readout, and memory access means for recognizing
the binary logic signal in a second state as an access
15 permission signal and executing the memory access to
the data memory means at a timing corresponding to the
clock signal when the logic signal is a second state.